

L Number	Hits	Search Text	DB	Time stamp
1	428758	(validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:40
2	6585863	PLC system or programmable logic controller	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:42
3	2967152	processing module or processor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:42
6	6120922	I/O module\$1 or system bus	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:44
7	152100	adapter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:44
8	62090	(processing module or processor\$1) and adapter\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:45
9	11776	(I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:45
10	17445	((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:46
11	387	((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:46
12	1893016	generat\$3 and (pre-specified voltage level or predetermined voltage level)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:47
13	253	((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:47
14	3943246	powered up	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:47

15	222	(((((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))) and (powered up)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:48
16	1181650	detect\$3 and (desired voltage level)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:49
17	151	(((((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))) and (powered up)) and (detect\$3 and (desired voltage level))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:49
18	4155179	error signal\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:49
19	27	(((((validat\$3 near1 connection near2 multi-wired cable) and (first and second electrical component\$1)) and (((I/O module\$1 or system bus) and ((processing module or processor\$1) and adapter\$1)) and (PLC system or programmable logic controller))) and (generat\$3 and (pre-specified voltage level or predetermined voltage level))) and (powered up)) and (detect\$3 and (desired voltage level))) and (error signal\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/05 12:49

12	15550	validation and protection circuitry near2 processor\$1	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/04 17:09
13	3639944	interlock circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/04 17:09
14	12	(((((test\$3 and (pre-specified voltage level with component\$1)) and (714/14 or 714/22 or 714/30 or 714/36 or 714/48 or 714/721 or 714/724 or 714/733 or 714/734 or 714/799)) and (built-in power supply)) and (assert\$3 and (error signal\$1 near2 component\$1 with detected pre-specified voltage level\$1))) and ((first and second components) and (powered up with pre-specified voltage level))) and (multi-wired cable)) and (validation and protection circuitry near2 processor\$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/04 17:09
15	419309	powered adapter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/04 17:09
17	4344699	error signal indicator	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/04 17:10
16	12	(((((test\$3 and (pre-specified voltage level with component\$1)) and (714/14 or 714/22 or 714/30 or 714/36 or 714/48 or 714/721 or 714/724 or 714/733 or 714/734 or 714/799)) and (built-in power supply)) and (assert\$3 and (error signal\$1 near2 component\$1 with detected pre-specified voltage level\$1))) and ((first and second components) and (powered up with pre-specified voltage level))) and (multi-wired cable)) and (validation and protection circuitry near2 processor\$1)) and (powered adapter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/04 18:01